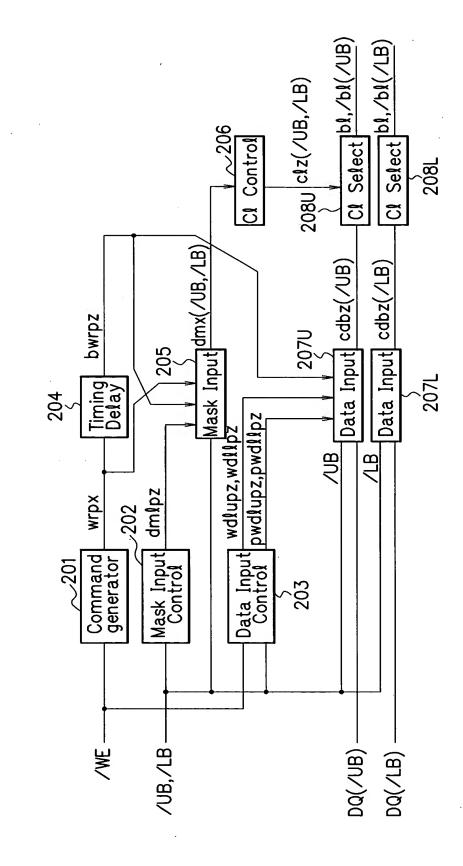
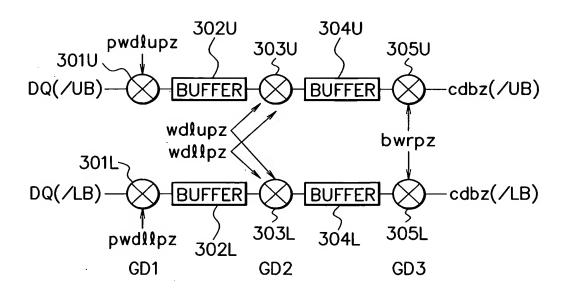


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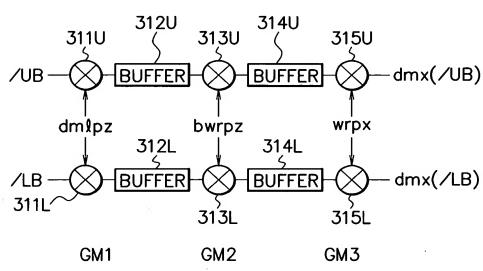


F I G. 2

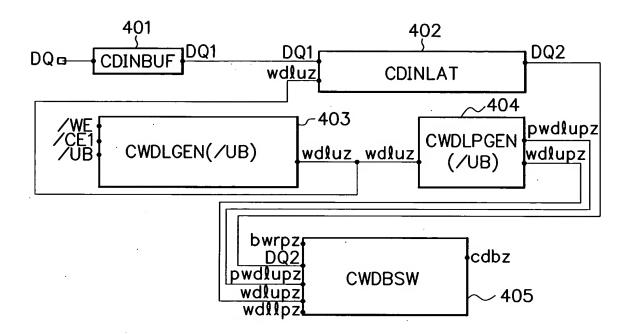
FIG. 3A



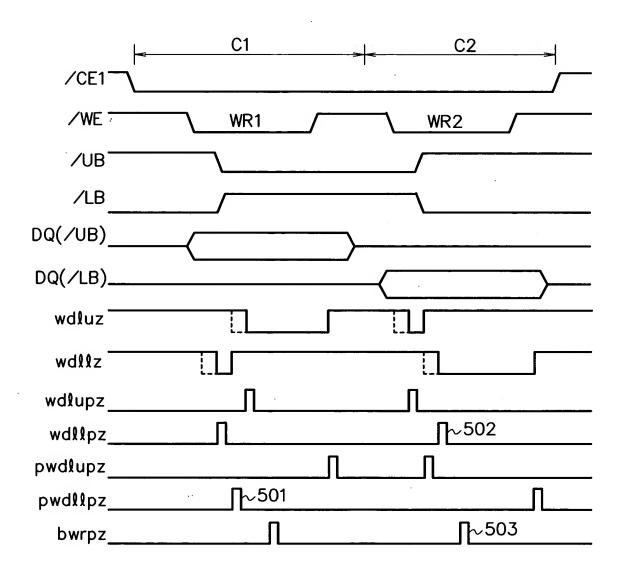
F I G. 3B



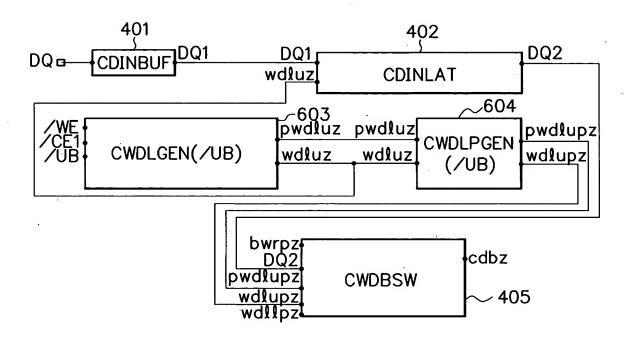
F I G. 4



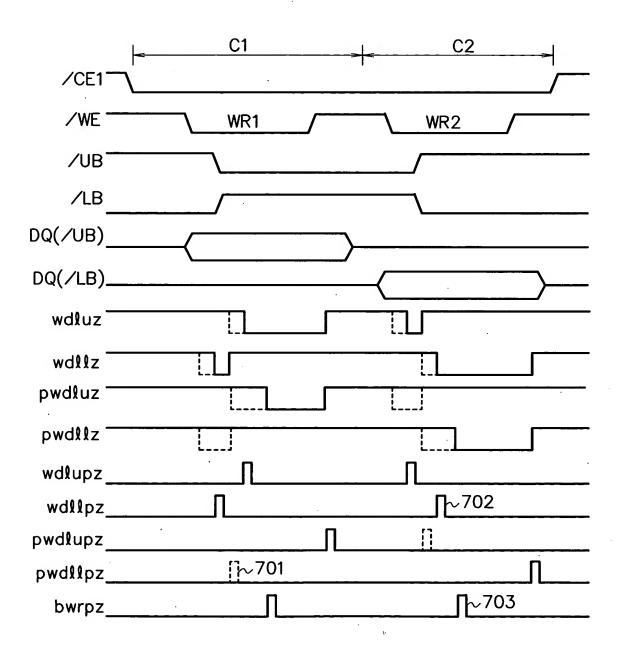
F I G. 5

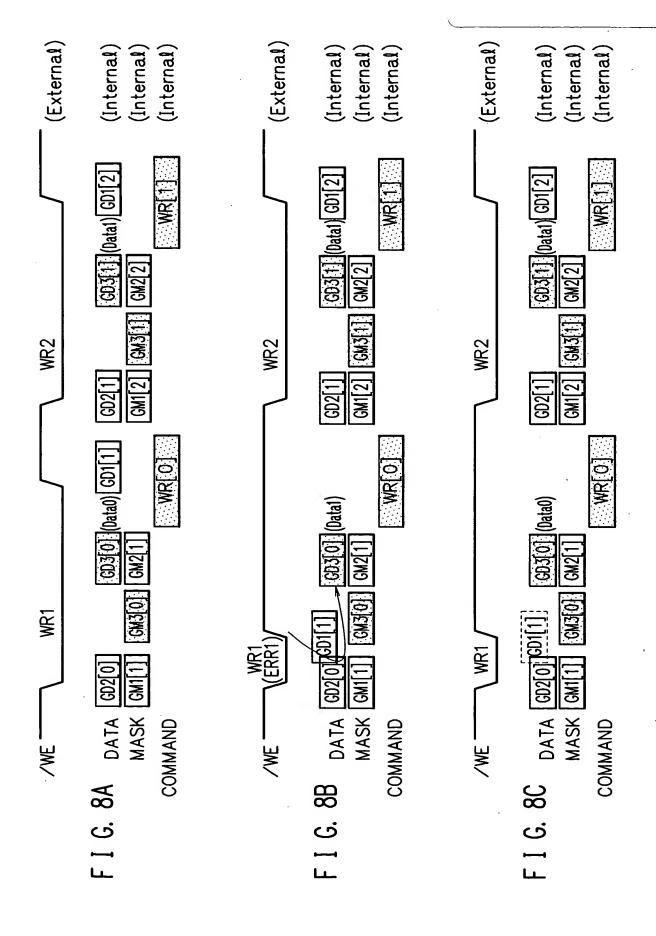


F I G. 6

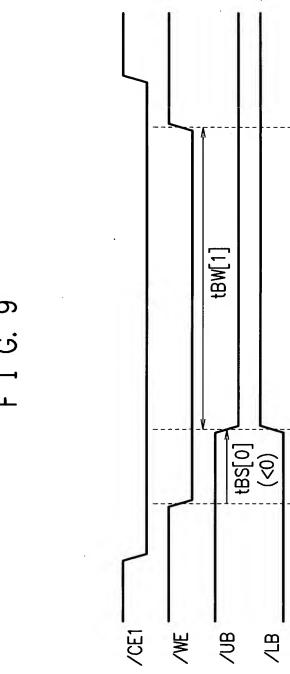


F I G. 7





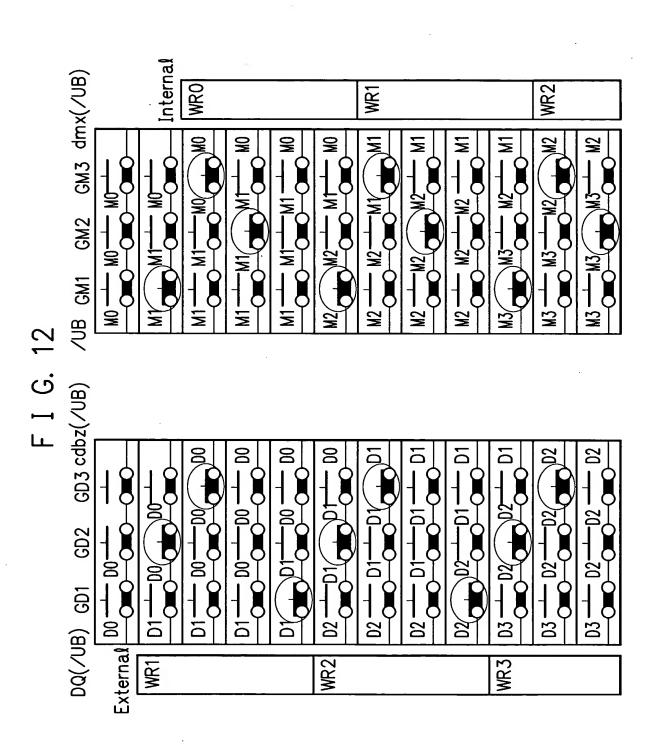
Title: MEMORY DEVICE Inventor: Takahiko SATO Docket Number: 108390-00062

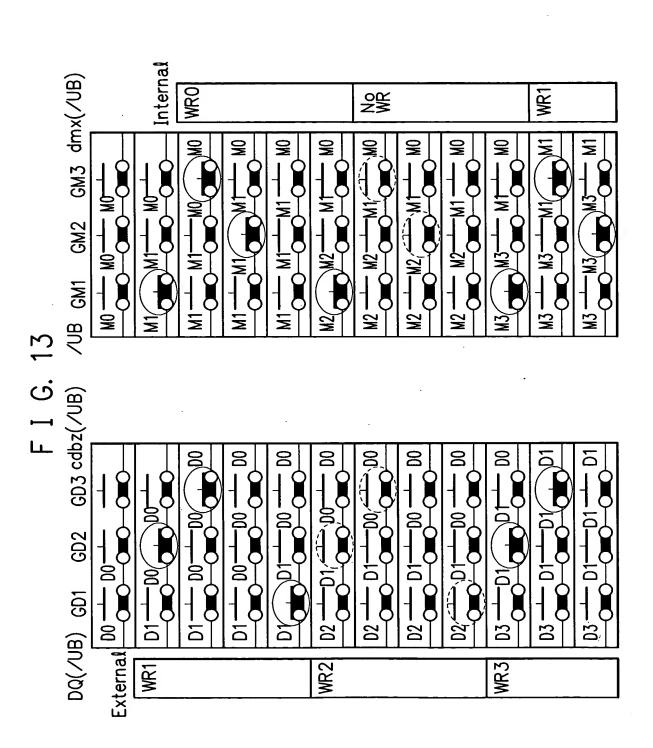


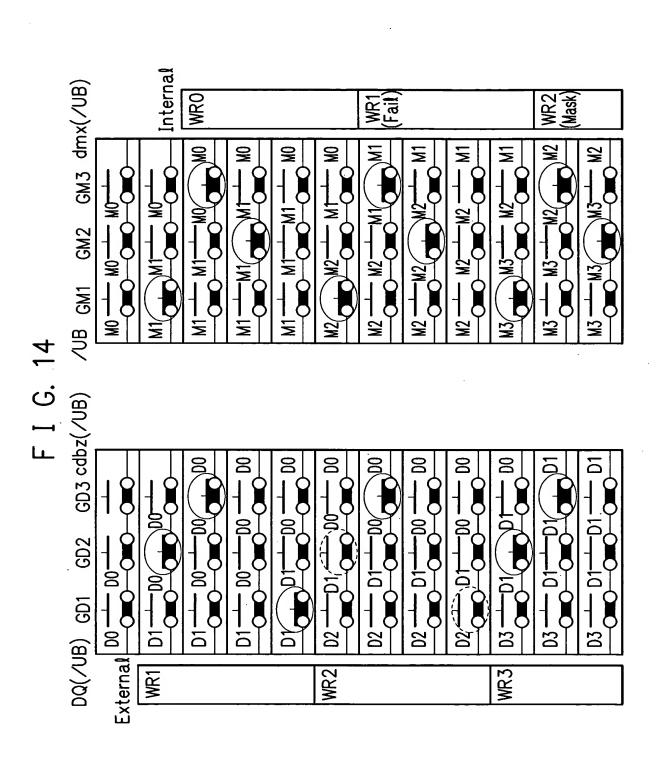
		MASK RECEIVE						·	MASK RECEIVE			
OPERATIONAL MODE					0 tBS(ns)		OPERATIONAL MODE					0 tBS(ns)
NO WR	WR OPERATION	/UB =" H" RECEIVE	DATA TRANSFER	DATA RECEIVE		NO WR	-	WR OPERATION	/UB =" H" RECEIVE	DATA TRANSFER	DATA RECEIVE	
DATA CORRUPTION 4 3a 2a		UNDETNED				DATA CORRUPTION	4 3b 2b		UNDETINED			
₩	WR [UB] EXECUTION	/UB ="L" RECEIVE	TRANSFER EXECUTION	RECEIVE EXECUTION		SK.	5	WR [UB] EXECUTION	/UB ="L" RECEIVE	TRANSFER EXECUTION	RECEIVE EXECUTION	
10A	GD3 [GM1	GD2 [601	ı	108		GD3 [GM1	GD2 [GD1	I
. I G. 10A						. I G. 10B						

WR2 WR1 **D3** WR3 **M**3 (WR2) **D**2 M_2 WR1 No WR WR0 WR0 WR1 Ξ ᄓ . MB/ 1B o O O WE.

F. I. G. 1







GM3 dmx(/UB) WRO Š₹ Fail GM2 /UB GM1 GD3 cdbz(/UB) DQ(/UB) GD1 GD2 External WR2 WR3 WR1

			MASK RECEIVE				
	OPERATIONAL MODE					0 tBS(ns)	
NO WR	1	WR OPERATION	/JB ="H" RECEIVE	DATA TRANSFER	DATA TRANSFER DATA RECEIVE		
	2c	·	₹				
	2d 2c						
	3]]	
DATA CORRUPTION	4		UNDEFINED				
W.	5	WR [UB] EXECUTION	/UB ="L" RECEIVE	TRANSFER EXECUTION	RECEIVE EXECUTION		
L		GD3	GM T	GD2	1 C	J	

